Notice of References Cited

Application/Control No.

10/716,907

Examiner

William H. Wood

Applicant(s)/Patent Under
Reexamination
POZZI ET AL.

Art Unit
Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	С	US-			
	D	US-			
	Ε	US-			
	F	US-			
	O	US-			
	Н	US-			
	1	US-			
	J	US-			
	к	US-			
	L	US-	,		
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	P					;
	α					
	R					
	S			,		
	Т					

NON-PATENT DOCUMENTS

*.		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	υ	Laura Pozzi, Miljan Vuletic, and Paolo Ienne. Automatic topology-based Identification of instruction-set Extensions for Embedded Processors, Poster paper in Proceedings of the Design, Automation and Test in Europe Conference and Exhibition, page 1138, Paris, March 2002				
	٧	A.Alippi, W.Fornaciari, L.Pozzi, M.G.Sami, Determining the Optimum Extended Instruction-Set Architecture for Application-Specific Reconfigurable VLIW CPUs, RSP'01 - 12th IEEE International Workshop on Rapid System Prototyping, Monterey, CA, June 25-27, 2001				
	w	Ajay Kumar Verma, Kubilay Atasu, Miljan Vuletic, Laura Pozzi, and Paolo lenne. Automatic Application-Specific Instruction-Set Extensions under Microarchitectural Constraints, In Proceedings of WASP-1 the 1st Workshop on				
	x	Peymandoust, A. Pozzi, L. lenne, P. De Micheli, G. Automatic Instruction Set Extension and Utilization for Embedded Processors. Application-Specific Systems, Architectures, and Processors, 2003. Proceedings. IEEE International Conference on Publication Date: 24-26 June 2003. page(s): 108 - 118				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination POZZI ET AL. Examiner | Art Unit | Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			•
	С	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	i	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Ν			•		
	0					
	P					
	α					
	R					·
	S					
	Т	,				

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Atasu, K. Pozzi, L. Ienne, P. Automatic Application-Specific Instruction-Set Extensions Under Microarchitectural Constraints. Design Automation Conference, 2003. Proceedings. Publication Date: 2-6 June 2003. page(s): 256 - 261			
	٧	•			
	w				
ļ	x				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

1 . h. . .